

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	(ring adj oscillator) and inverter and power adj line and delay adj line and vco and pll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:03
L2	1	"10/054964"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:02
L3	9	(ring adj oscillator) and inverter and (power adj supply adj line) and delay adj line and vco and pll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:20
L4	2	"6333652".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:12
L5	117	(ring adj oscillator) and inverter and (power with line) and delay adj line and (vco or (voltage adj controlled adj oscillat\$3)) and pll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:34
L6	60	(ring adj oscillator) and (second adj inverter) and (power with line) and (delay adj line) and (vco or (voltage adj controlled adj oscillat\$3)) and pll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:42
L7	8	(ring adj oscillator) and inverter and (power with line) and delay adj line and (vco or (voltage adj controlled adj oscillat\$3)) and pll and (sampling adj clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:43
L8	1	(ring adj oscillator) same inverter same (power with line) same (delay adj line) same (vco or (voltage adj controlled adj oscillat\$3)) same pll same (sampling adj clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:40

EAST Search History

L9	1	(ring adj oscillator) same inverter same (power with line) same (delay adj line) same (vco or (voltage adj controlled adj oscillat\$3)) same pll same (sampling with clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:41
L10	1	((ring adj oscillator) and inverter and (power with line) and delay adj line and (vco or (voltage adj controlled adj oscillat\$3)) and pll and (sampling adj clock)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:41
L11	4339	375/376	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:41
L12	1791	375/373	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:41
L13	14	5 and 11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:42
L14	7	5 and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:45
L15	83	(ring adj oscillator) and inverter and (power with line) and delay adj line and (vco or (voltage adj controlled adj oscillat\$3)) and pll and (sampl\$3 with clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:44
L16	62	(ring adj oscillator) and (second with inverter) and (power with line) and delay adj line and (vco or (voltage adj controlled adj oscillat\$3)) and pll and (sampl\$3 with clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:46

EAST Search History

L17	2	16 and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:45
L18	2	16 and 11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:44
L19	1	(ring adj oscillator) and ((second with inverter) with serial with parallel) and (power with line) and delay adj line and (vco or (voltage adj controlled adj oscillat\$3)) and pll and (sampl\$3 with clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:47
L20	1	(ring adj oscillator) and ((inverter) with serial with parallel) and (power with line) and delay adj line and (vco or (voltage adj controlled adj oscillat\$3)) and pll and (sampl\$3 with clock)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 08:47
L21	1	(ring adj oscillator) and ((second with inverter with (sampl\$3 with clock)) with serial with parallel) and (power with line) and delay adj line and (vco or (voltage adj controlled adj oscillat\$3)) and pll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:06
L24	623	(ring adj oscillator) and serial same parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:06
L25	220	(ring adj oscillator) and (serial same parallel same convert\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:06
L26	183	(ring adj oscillator) and (serial with parallel with convert\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:06

EAST Search History

L27	95	(ring adj oscillator) and (serial with parallel with convert\$2) and inverter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:07
L28	36	(ring adj oscillator) and (serial with parallel with convert\$2) and (second with inverter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:07
L29	13	(ring adj oscillator) and (serial with parallel with convert\$2) and (second adj inverter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 09:07
L30	0	liquid adj cristal and ring adj oscillator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 10:30
L31	354	liquid adj crystal and ring adj oscillator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 10:31
L32	2	liquid adj crystal and ring adj oscillator and "serial/parallel"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 10:32
L33	40	(liquid adj crystal) and (ring adj oscillator) and serial and parallel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 10:33
L34	31	(liquid adj crystal) and (ring adj oscillator) and serial and parallel and inverter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 10:33

EAST Search History

L35	6	(liquid adj crystal) and (ring adj oscillator) and serial and parallel and inverter and pll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 10:35
L36	29	(liquid adj crystal) and (ring adj oscillator) and inverter and pll	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 10:35
L37	276065	(liquid adj crystal) and (ring adj oscillator) and inverter and pll ans delay adj line	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 10:35
L38	6	(liquid adj crystal) and (ring adj oscillator) and inverter and pll and delay adj line	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 10:36
L39	1868	331/57	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 10:52
L40	1	16 and 39	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/14 10:52

PALM Intranet

Application
Number

IDS Flag Clearance for Application 10054964

**IDS
Information**

Content	Mailroom Date	Entry Number	IDS Review	Last Modified	Reviewer
<input type="button" value="Update"/>					

 PALM INTRANETDay : Tuesday
Date: 11/14/2006
Time: 07:29:56

Continuity Information for 10/054964

Parent Data

No Parent Data

Child Data

No Child Data

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign Data](#)[Inve](#)**Search Another: Application#** **or Patent#** **PCT /****/** **or PG PUBS #** **Attorney Docket #****Bar Code #**

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

 PALM INTRANETDay : Tuesday
Date: 11/14/2006
Time: 07:30:00

Foreign Information for 10/054964

Priority#	Date	Country
2001-023467	01/31/2001	JAPAN

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)**Foreign
Data**[Inventor](#)

Search Another: Application#

or Patent#

PCT /

or PG PUBS #

Attorney Docket #

Bar Code #

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

 PALM INTRANETDay : Tuesday
Date: 11/14/2006
Time: 07:30:05

Inventor Information for 10/054964

Inventor Name	City	State/Country
SUMIYOSHI, NOBUYA	KYOTO	JAPAN

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign Data](#)

Search Another: Application#

or Patent#

PCT /


or PG PUBS #

Attorney Docket #

Bar Code #

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

 PALM INTRANET

Day : Tuesday
Date: 11/14/2006
Time: 07:30:09**Inventor Name Search Result**

Your Search was:

Last Name = SUMIYOSHI

First Name = NOBUYA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10054964	Not Issued	30	01/25/2002	Sampling clock generator circuit and data receiver using the same	SUMIYOSHI, NOBUYA
10648484	Not Issued	93	08/27/2003	REGENERATOR CIRCUIT OF SERIAL DATA AND METHOD OF REGENERATING THE SAME	SUMIYOSHI, NOBUYA

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name
<input type="text" value="SUMIYOSHI"/>	<input type="text" value="NOBUYA"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

**PALM INTRANET**Day : Tuesday
Date: 11/14/2006
Time: 07:31:21

Correspondence Address for 10/054964

Customer Number	Contact Information	Address
No Customer #	Telephone: (703)684-1120 Fax: No Fax # E-Mail: No E-Mail Address	MATTINGLY, STANGER & MALUR, P.C. Suite 370 1800 Diagonal Road Alexandria VA 22314

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign Data](#)

Search Another: Application#

or Patent#

PCT /

/

or PG PUBS #

Attorney Docket #

Bar Code #

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

[Google](#)

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

"ring oscillator" "second inverters" "sampling cl

Search

[Advanced Search](#)
[Preferences](#)

Web

Tip: Try removing quotes from your search to get more results.

Your search - **"ring oscillator" "second inverters" "sampling clock" power line" "delay line" "voltage controlled oscillator" pll** - did not match any documents.

Suggestions:

- Make sure all words are spelled correctly.
- Try different keywords.
- Try more general keywords.
- Try fewer keywords.

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2006 Google

Google

Web Images Video News Maps more »

"ring oscillator" inverters "sampling clock" pow.

Search

Advanced Search
Preferences

Web Results 1 - 10 of about 81 for "**ring oscillator**" inverters "**sampling clock**" power line" "**delay line**" "**voltage controlled oscillator**"

Scholarly articles for "**ring oscillator**" inverters "**sampling clock**" power line" "**delay line**" "**voltage controlled oscillator**" pll



High-speed electrical signaling: overview and limitations - Horowitz - Cited by 67

A 2.5-10-Gb/s CMOS transceiver with alternating ... - Lee - Cited by 15

A 0.8 m CMOS 2.5 Gb/s oversampling receiver and ... - Yang - Cited by 56

[PDF] Top-Down Design of a Low-Power Multi-Channel 2.5-Gbit/s/Channel ...

File Format: PDF/Adobe Acrobat

delay line and the **ring oscillator** are built with identical. current-mode logic two-input gates. ... cdr_gcco_cc0: **voltage**; -- **Control** current mid-point [C] ...

ieeexplore.ieee.org/iel5/9609/30361/01395567.pdf - [Similar pages](#)

[PDF] A High Precision-(+/-100ppm) Cmos Clock Generator For Opt131lm ...

File Format: PDF/Adobe Acrobat

able **delay line**, a **PLL** and their **control** logic. The function. diagram and a chip photograph are shown in Fig.2 and ... The VCO, a **ring oscillator** type, con- ...

ieeexplore.ieee.org/iel2/4489/12733/00590803.pdf?arnumber=590803 - [Similar pages](#)

[[More results from ieeexplore.ieee.org](#)]

High-Speed Electrical Signaling: Overview and Limitations

The VCO and the VCDL comprise six **voltage-controlled delay** elements, ... The simplest is a **ring oscillator** (or a **delay line** with its phase input locked to ...

doi.ieeecomputersociety.org/10.1109/40.653013 - [Similar pages](#)

[PDF] High Speed Electrical Signalling: Overview and Limitations

File Format: PDF/Adobe Acrobat - [View as HTML](#)

output clock with a VCO, DLL's use a **Voltage Controlled Delay Line** (VCDL) ... The simplest is to use a **ring oscillator** (or a **delay line** with its phase input ...

www.vlsi.stanford.edu/papers/mh_micro_98.pdf - [Similar pages](#)

Voltage-controlled ring oscillator using complementary ...

M. G. Johnson & E. L. Hudson, "A Variable **Delay Line PLL** for ... 5 is a schematic diagram of a **voltage-controlled ring oscillator** employing an even number ...

www.freepatentsonline.com/5239274.html - 47k - [Cached](#) - [Similar pages](#)

Digital frequency multiplier utilizing digital controlled ...

A variable **delay** element, preferably a group of tri-state **inverters** connected in ... The output of **ring oscillator** 16 on a **line** 17 is also the output of ...

www.freepatentsonline.com/5438300.html - 48k - [Cached](#) - [Similar pages](#)

«Session» - «SessTime»

A Time-Slicing **Ring Oscillator** for Capturing Instantaneous **Delay** Degradation ... Match **Line Sense Amplifiers** with Positive Feedback for Low-Power Content ...

www.ieee-cicc.org/Sessions-Complete.htm - 517k - [Cached](#) - [Similar pages](#)

[PDF] A 0.8-/spl mu/m CMOS 2.5 Gb/s oversampling receiver and ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

voltage using a scaled charge pump [4]. The **control voltage** ... clocks from the **ring oscillator** is buffered to match the **delay** ...

mos.stanford.edu/papers/cky_jssc_12_96.pdf - [Similar pages](#)

[PDF] AN EFFICIENT I/O AND CLOCK RECOVERY DESIGN FOR TERABIT INTEGRATED ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

delay line (VCDL) in a **delay**-locked loop (DLL) or the **voltage-controlled oscillator**.

(VCO) in a phase-locked loop (**PLL**) [6] [8] [11]. This type of **delay** ...

cva.stanford.edu/publications/2001/elee_thesis.pdf - [Similar pages](#)

[PDF] [CMOS high-speed I/Os - present and future - Computer Design, 2003 ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

mance, independent of the length of the **line**. At high data ... Analysis of a CMOS **inverter**

ring oscillator suggests that. clock jitter can be made to scale ...

cva.stanford.edu/publications/2003/lee_future.pdf - [Similar pages](#)

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [Next](#)

Try [Google Desktop](#): search your computer as easily as you search the web.

"ring oscillator" inverters "sampling c"

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2006 Google

[Google](#)

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

"ring oscillator" inverters "sampling clock" pow:

[Advanced Search](#)
[Preferences](#)

Web

Tip: Try removing quotes from your search to get more results.

Your search - **"ring oscillator" inverters "sampling clock" power line" "delay line" "voltage controlled oscillator" pll serial parallel** - did not match any documents.

Suggestions:

- Make sure all words are spelled correctly.
- Try different keywords.
- Try more general keywords.
- Try fewer keywords.

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2006 Google



About Us

Newsroom

Advisory Board

Submit Web Site

Help

Contact Us

Basic Search

[Advanced Search](#) [Search Preferences](#)

"ring oscillator" AND inverters AND "sampling clock" Al

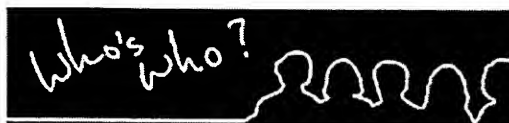
Search

☒ Journal sources ☒ Preferred Web sources ☒ Other Web sources ☐ Exact phrase

Sorry, your search has not produced any results. Please check the syntax of your query.

[Downloads](#) | [Subscribe to News Updates](#) | [User Feedback](#) | [Advertising](#)
[Tell A Friend](#) | [Terms Of Service](#) | [Privacy Policy](#) | [Legal](#)

Powered by FAST © Elsevier 2006



About Us

Newsroom

Advisory Board

Submit Web Site

Help

Contact Us

Basic Search

[Advanced Search](#) [Search Preferences](#)

"ring oscillator" AND inverters AND "sampling clock" Al

Search

☒ Journal sources ☒ Preferred Web sources ☒ Other Web sources ☐ Exact phrase

Sorry, your search has not produced any results. Please check the syntax of your query.

[Downloads](#) | [Subscribe to News Updates](#) | [User Feedback](#) | [Advertising](#)
[Tell A Friend](#) | [Terms Of Service](#) | [Privacy Policy](#) | [Legal](#)

Powered by [FAST](#) © Elsevier 2006

About Us

Newsroom

Advisory Board

Submit Web Site

Help

Contact Us

Basic Search

[Advanced Search](#) [Search Preferences](#)

"ring oscillator" AND inverters AND "sampling clock" AND

Search

☒ Journal sources ☒ Preferred Web sources ☒ Other Web sources ☐ Exact phrase

Searched for:: :All of the words:"ring oscillator" AND inverters AND "sampling clock" AND "power line" AND "de

Found:: :5 total | 0 journal results | 1 preferred web results | 4 other web results

Sort by:: :relevance | date

Save checked results

Email checked results

Export checked results

Refine your search
using these keywords
found in the results:

[clock generation](#)

[data rate](#)

[phase detector](#)

[phase error](#)

[phase noise](#)

[transmitter](#)

Or refine using:

All of the words

Refine

- ☐ 1. [High speed submicron CMOS oscillators and PLL clock generators](#)
Sun, Lizhong, Jan 1999
...sub-feedback **inverters** to constnct...fiequency of the **ring oscillator** is
directly...of sub-fdback **inverters** which can be...VCO and the **PLL** for
applications...oscillatorwith N stage **inverters**...26 DLL **PLL** clock
anddata...A four stage **ring oscillator** with quadrature...
Full text thesis available via NDLTD
[similar results](#)
- ☐ 2. [DESIGN OF HIGH - SPEED SERIAL](#) [PDF-340K]
Oct 2001
...phases tapped from a local **ring oscillator** are driven to a bank...69
Figure 4.1: **PLL** block diagram...2: Voltage-controlled **ring oscillator**
schematic...2nd-order charge pump **PLL** block diagram...
[http://velox.stanford.edu/papers/cky_thesis.pdf]
[similar results](#)
- ☐ 3. [DESIGN OF HIGH - SPEED SERIAL](#) [PDF-338K]
Oct 2001
...phases tapped from a local **ring oscillator** are driven to a bank...69
Figure 4.1: **PLL** block diagram...2: Voltage-controlled **ring oscillator**
schematic...2nd-order charge pump **PLL** block diagram...
[more hits from](#) [http://mos.stanford.edu/papers/cky_thesis.pdf]
[similar results](#)
- ☐ 4. [DESIGN OF HIGH - SPEED SERIAL](#) [PDF-320K]
Oct 2001
...controlled phases tapped from a local **ring oscillator** are driven to a
bank of input...69 Figure 4.1: **PLL** block diagram...Figure 4.2: Voltage-
controlled **ring oscillator** schematic...
[http://www-vlsi.stanford.edu/papers/cky_thesis.pdf]
[similar results](#)
- ☐ 5. [stef.EPS](#) [PDF-71K]
Sep 2000
...Clock and data recovery, **PLL**'s 1 High Speed Electrical...of one stage in a
chain of **inverters**, where each of the **inverters**...of phase uncertainty in
the **sampling clock**, static input offset of the...D2 D7 clk0 clk1 clk2 clk3
Ring Oscillator clk0 clk1 clk2 clk3 ck0 ck1...

[http://mos.stanford.edu/papers/mh_micro_98.pdf]
[similar results](#)

Sponsored links

[The Inverter Store](#)

Low Priced **Inverters**. Free Ship. Buy Here. 888-417-8673. Up to 7000W
www.TheInverterStore.com

[Inverters R Us](#)

One of the largest & most trusted! New 25W to 7000W Call US today
www.invertersRus.com



[Downloads](#) | [Subscribe to News Updates](#) | [User Feedback](#) | [Advertising](#)
[Tell A Friend](#) | [Terms Of Service](#) | [Privacy Policy](#) | [Legal](#)

[Powered by FAST](#) © Elsevier 2006

SCIRUS
for scientific information only

SCOPUSTM
Find out.

About Us

Newsroom

Advisory Board

Submit Web Site

Help

Contact Us

Basic Search

[Advanced Search](#) [Search Preferences](#)

"ring oscillator" AND "second inverters" AND "sampling

Search

☒ Journal sources ☒ Preferred Web sources ☒ Other Web sources ☐ Exact phrase

Sorry, your search has not produced any results. Before searching again, using the same or similar keywords, you may find it helpful to:

- check the selected sources, information types and subject areas, the selection may not contain results matching your query
 - check the spelling of all words
 - spell words in a different way, for example using American spelling
 - write abbreviations and acronyms in full
 - use alternative words that have the same meaning
 - search using fewer or more general words
-

[Downloads](#) | [Subscribe to News Updates](#) | [User Feedback](#) | [Advertising](#)
[Tell A Friend](#) | [Terms Of Service](#) | [Privacy Policy](#) | [Legal](#)

Powered by FAST © Elsevier 2006



Welcome United States Patent and Trademark Office

□ Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)[SUPPORT](#)

Results for "((ring oscillator<in>metadata) <and> (inverter<in>metadata))<and> (sa..."

Your search matched 0 documents.

e-mail printer

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

[View Session History](#)[New Search](#)

Modify Search

 ☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revisin search.

Indexed by
 Inspec[Help](#) [Contact Us](#) [Privacy & Security](#)

© Copyright 2006 IEEE - All Rights



Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Results for "(((ring oscillator<in>metadata) <and> (inverter<in>metadata))) <and> (..."

e-mail
 printer

Your search matched **142** of **1431298** documents.A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

» Search Options

[View Session History](#)
[New Search](#)

Modify Search

☐ Check to search only within this results set
Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

[Select All](#) [Deselect All](#)
View: 1-25 | [26-50](#) | [51-75](#)

- ☐ 1. **Ring oscillator using InAlAs/InGaAs/InP enhancement/depletion-mode high electron mobility transistor direct-coupled FET logic inverters**
Cueva, G.; Mahajan, A.; Fay, P.; Arafat, M.; Adesida, I.;
[Indium Phosphide and Related Materials, 1997., International Conference on](#)
11-15 May 1997 Page(s):157 - 160
Digital Object Identifier 10.1109/ICIPRM.1997.600078
[AbstractPlus](#) | Full Text: [PDF\(260 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 2. **Characteristics of GaAs buffered FET logic (BFL) MESFETs and inverters exposed high-energy neutrons**
Rosenbluth, M.; Bloss, W.L.; Yamada, W.E.; Janousek, B.K.;
[Nuclear Science, IEEE Transactions on](#)
Volume 38, Issue 1, Feb. 1991 Page(s):20 - 24
Digital Object Identifier 10.1109/23.64632
[AbstractPlus](#) | Full Text: [PDF\(456 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 3. **A novel high-speed ring oscillator for multiphase clock generation using negative delay scheme**
Seog-Jun Lee; Beomsup Kim; Kwiro Lee;
[Solid-State Circuits, IEEE Journal of](#)
Volume 32, Issue 2, Feb. 1997 Page(s):289 - 291
Digital Object Identifier 10.1109/4.551926
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(48 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 4. **High temperature performance of NMOS integrated inverters and ring oscillators in SiC**
Schmid, U.; Sheppard, S.T.; Wondrak, W.;
[Electron Devices, IEEE Transactions on](#)
Volume 47, Issue 4, April 2000 Page(s):687 - 691
Digital Object Identifier 10.1109/16.830980
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(112 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 5. **Characteristics of GaAs heterojunction FETs (HFETs) and source follower FET log (SFFL) inverters exposed to high energy neutrons**
Janousek, B.K.; Krantz, R.J.; Bloss, W.L.; Yamada, W.E.; Brown, S.; Remke, R.L.; Witm
[Nuclear Science, IEEE Transactions on](#)
Volume 36, Issue 6, Part 1-2, Dec. 1989 Page(s):2223 - 2228

Digital Object Identifier 10.1109/23.45428

[AbstractPlus](#) | Full Text: [PDF\(352 KB\)](#) IEEE JNL

[Rights and Permissions](#)

6. **Monolithic process for co-integration of GaAs MESFET and silicon CMOS devices circuits**
Shichijo, H.; Matyi, R.; Taddiken, A.H.; Kao, Y.-C.;
[Electron Devices, IEEE Transactions on](#)
Volume 37, Issue 3, Part 1, March 1990 Page(s):548 - 555
Digital Object Identifier 10.1109/16.47756
[AbstractPlus](#) | Full Text: [PDF\(1136 KB\)](#) IEEE JNL
[Rights and Permissions](#)
7. **Ring oscillators with monolithically integrated-optical readout based on GaAs-AlG FET-SEED technology**
Woodward, T.K.; Novotny, R.A.; Lentine, A.L.; Chirovsky, L.M.F.; D'Asaro, L.A.; Hui, S.; M.W.; Guth, G.D.; Smith, L.E.; Leibenguth, R.E.;
[Electron Device Letters, IEEE](#)
Volume 16, Issue 2, Feb. 1995 Page(s):52 - 54
Digital Object Identifier 10.1109/55.386030
[AbstractPlus](#) | Full Text: [PDF\(280 KB\)](#) IEEE JNL
[Rights and Permissions](#)
8. **Demonstration of sub-5 ps CML ring oscillator gate delay with reduced parasitic AlInAs/InGaAs HBT**
Sokolich, M.; Kramer, A.R.; Boegeman, Y.K.; Martinez, R.R.;
[Electron Device Letters, IEEE](#)
Volume 22, Issue 7, July 2001 Page(s):309 - 311
Digital Object Identifier 10.1109/55.930674
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(160 KB\)](#) IEEE JNL
[Rights and Permissions](#)
9. **A quadrature output voltage controlled ring oscillator based on three-stage sub-fe loops**
Lizhong Sun; Kwasniewski, T.; Iniewski, K.;
[Circuits and Systems, 1999. ISCAS '99. Proceedings of the 1999 IEEE International Syn on](#)
Volume 2, 30 May-2 June 1999 Page(s):176 - 179 vol.2
Digital Object Identifier 10.1109/ISCAS.1999.780647
[AbstractPlus](#) | Full Text: [PDF\(308 KB\)](#) IEEE CNF
[Rights and Permissions](#)
10. **Characteristics of GaAs MESFET inverters exposed to high energy neutrons**
Bloss, W.L.; Yamada, W.E.; Young, A.M.; Janousek, B.K.;
[Nuclear Science, IEEE Transactions on](#)
Volume 35, Issue 5, Oct. 1988 Page(s):1074 - 1079
Digital Object Identifier 10.1109/23.7501
[AbstractPlus](#) | Full Text: [PDF\(444 KB\)](#) IEEE JNL
[Rights and Permissions](#)
11. **A 1.25-GHz 0.35- μ m monolithic CMOS PLL based on a multiphase ring oscillator**
Lizhong Sun; Kwasniewski, T.A.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 36, Issue 6, June 2001 Page(s):910 - 916
Digital Object Identifier 10.1109/4.924853
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(168 KB\)](#) IEEE JNL
[Rights and Permissions](#)
12. **AlGaAs/GaAs based HEMTs, inverters and ring oscillators with InGaAs and AlGaAs stop layers**
Ren, F.; Pearton, S.J.; Kopf, R.F.; Chu, S.N.G.; Pei, S.S.;
[Electronics Letters](#)

Volume 27, Issue 13, 20 June 1991 Page(s):1175 - 1177

[AbstractPlus](#) | Full Text: [PDF\(248 KB\)](#) IEEE JNL

13. **Higher harmonic generation in CMOS/SOS ring oscillators**
Sasaki, N.;
[Electron Devices, IEEE Transactions on](#)
Volume 29, Issue 2, Feb 1982 Page(s):280 - 283
[AbstractPlus](#) | Full Text: [PDF\(408 KB\)](#) IEEE JNL
[Rights and Permissions](#)
14. **CMOS on buried nitride—A VLSI SOI technology**
Zimmer, G.; Vogt, H.;
[Electron Devices, IEEE Transactions on](#)
Volume 30, Issue 11, Nov 1983 Page(s):1515 - 1520
[AbstractPlus](#) | Full Text: [PDF\(1296 KB\)](#) IEEE JNL
[Rights and Permissions](#)
15. **Ring oscillator experiment using a huffle circuit**
Kotera, N.; Asano, A.; Harada, Y.; Kawabe, U.;
[Magnetics, IEEE Transactions on](#)
Volume 19, Issue 3, May 1983 Page(s):1174 - 1177
[AbstractPlus](#) | Full Text: [PDF\(520 KB\)](#) IEEE JNL
[Rights and Permissions](#)
16. **A process and temperature compensated ring oscillator**
Yang-Shyung Shyu; Jiin-Chuan Wu;
[ASICs, 1999. AP-ASIC '99. The First IEEE Asia Pacific Conference on](#)
23-25 Aug. 1999 Page(s):283 - 286
Digital Object Identifier 10.1109/APASIC.1999.824084
[AbstractPlus](#) | Full Text: [PDF\(336 KB\)](#) IEEE CNF
[Rights and Permissions](#)
17. **High-temperature latchup characteristics in VLSI CMOS circuits**
Shoucair, F.S.;
[Electron Devices, IEEE Transactions on](#)
Volume 35, Issue 12, Dec 1988 Page(s):2424 - 2426
Digital Object Identifier 10.1109/16.8826
[AbstractPlus](#) | Full Text: [PDF\(268 KB\)](#) IEEE JNL
[Rights and Permissions](#)
18. **Vertical-type amorphous-silicon MOSFET ICs**
Okada, H.; Uchida, Y.; Arai, K.; Oda, S.; Matsumura, M.;
[Electron Devices, IEEE Transactions on](#)
Volume 35, Issue 7, Part 1, July 1988 Page(s):919 - 922
Digital Object Identifier 10.1109/16.3345
[AbstractPlus](#) | Full Text: [PDF\(352 KB\)](#) IEEE JNL
[Rights and Permissions](#)
19. **Characteristics of GaAs DCFL MESFET's and inverters exposed to high-energy ne**
Bloss, W.L.; Yamada, W.E.; Rosenbluth, M.L.; Janousek, B.K.;
[Nuclear Science, IEEE Transactions on](#)
Volume 36, Issue 6, Part 1-2, Dec. 1989 Page(s):2443 - 2449
Digital Object Identifier 10.1109/23.45459
[AbstractPlus](#) | Full Text: [PDF\(556 KB\)](#) IEEE JNL
[Rights and Permissions](#)
20. **Relating CMOS inverter lifetime to DC hot-carrier lifetime of NMOSFETs**
Lee, P.M.; Ko, P.K.; Hu, C.;
[Electron Device Letters, IEEE](#)
Volume 11, Issue 1, Jan. 1990 Page(s):39 - 41
Digital Object Identifier 10.1109/55.46924

[AbstractPlus](#) | Full Text: [PDF\(216 KB\)](#) IEEE JNL
[Rights and Permissions](#)

21. **Subfemtojoule deep submicrometer-gate CMOS built in ultra-thin Si film on SIMO substrates**
Miki, H.; Ohmameuda, T.; Kumon, M.; Asada, K.; Sugano, T.; Omura, Y.; Izumi, K.; Saki; [Electron Devices, IEEE Transactions on](#)
Volume 38, Issue 2, Feb. 1991 Page(s):373 - 377
Digital Object Identifier 10.1109/16.69919
[AbstractPlus](#) | Full Text: [PDF\(660 KB\)](#) IEEE JNL
[Rights and Permissions](#)
22. **Predicted performance of high-speed integrated-injection logic using InGaAs/InP heterojunction bipolar transistors**
Houston, P.A.; Lee, K.-C.;
[Electron Devices, IEEE Transactions on](#)
Volume 39, Issue 5, May 1992 Page(s):1080 - 1084
Digital Object Identifier 10.1109/16.129086
[AbstractPlus](#) | Full Text: [PDF\(448 KB\)](#) IEEE JNL
[Rights and Permissions](#)
23. **A high-performance 0.25- μ m CMOS technology. I. Design and characterization**
Chang, W.-H.; Davari, B.; Wordeman, M.R.; Taur, Y.; Hsu, C.C.-H.; Rodriguez, M.D.;
[Electron Devices, IEEE Transactions on](#)
Volume 39, Issue 4, April 1992 Page(s):959 - 966
Digital Object Identifier 10.1109/16.127489
[AbstractPlus](#) | Full Text: [PDF\(636 KB\)](#) IEEE JNL
[Rights and Permissions](#)
24. **21-ps 0.1- μ m CMOS devices operating at room temperature**
Izawa, T.; Watanabe, K.; Kawamura, S.;
[Electron Device Letters, IEEE](#)
Volume 14, Issue 11, Nov. 1993 Page(s):533 - 535
Digital Object Identifier 10.1109/55.258006
[AbstractPlus](#) | Full Text: [PDF\(236 KB\)](#) IEEE JNL
[Rights and Permissions](#)
25. **A proposed method for dynamic fitting of MOS model parameters**
Kovacs, F.; Hosszu, G.;
[Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on](#)
Volume 12, Issue 10, Oct. 1993 Page(s):1503 - 1507
Digital Object Identifier 10.1109/43.256924
[AbstractPlus](#) | Full Text: [PDF\(428 KB\)](#) IEEE JNL
[Rights and Permissions](#)

View: [1-25](#) | [26-50](#) | [51-75](#)

Indexed by
 Inspec®

[Help](#) [Contact Us](#) [Privacy & Security](#)

© Copyright 2006 IEEE – All Rights



Welcome United States Patent and Trademark Office

□ Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)[SUPPORT](#)

Results for "(((ring oscillator<in>metadata) <and> (second inverters<in>metadata))) <a..."

Your search matched 1 of 1431298 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

[e-mail](#) [printer](#)

» Search Options

[View Session History](#)[New Search](#)

Modify Search

(((ring oscillator<in>metadata) <and> (second inverters<in>metadata))) <and> (p) [Search](#) >☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

[view selected items](#) [Select All](#) [Deselect All](#)

- ☐ 1. **SOI/CMOS circuits fabricated in zone-melting-recrystallized Si films on SiO₂-coated substrates**
Tsaur, B.Y.; Fan, J.C.C.; Chapman, R.L.; Geis, M.W.; Silversmith, D.J.; Mountain, R.W.;
[Electron Device Letters, IEEE](#)
Volume 3, Issue 12, Dec 1982 Page(s):398 - 401
[AbstractPlus](#) | Full Text: [PDF\(1184 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#)

Indexed by
 Inspect®[Help](#) [Contact Us](#) [Privacy & Security](#)

© Copyright 2006 IEEE - All Rights



Welcome United States Patent and Trademark Office

□ Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)[SUPPORT](#)

Results for "(((ring oscillator<in>metadata) <and> (inverters<in>metadata))<and> (..."

Your search matched 0 documents.

[e-mail](#) [printer](#)A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.

» Search Options

[View Session History](#)[New Search](#)

Modify Search

Search☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revisin search.

Indexed by
 Inspect[Help](#) [Contact Us](#) [Privacy & Security](#)

© Copyright 2006 IEEE – All Rights



Welcome United States Patent and Trademark Office

□ Search Results

[BROWSE](#)[SEARCH](#)[IEEE XPLORE GUIDE](#)[SUPPORT](#)

Results for "(((ring oscillator<in>metadata) <and> (serial<in>metadata))<and> (par..."

[e-mail](#) [printer](#)Your search matched **0** documents.A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» Search Options

[View Session History](#)[New Search](#)

Modify Search

((((ring oscillator<in>metadata) <and> (serial<in>metadata))<and> (parallel<in>m

[Search](#) >☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revisin search.

Indexed by
 Inspect[®][Help](#) [Contact Us](#) [Privacy & Security](#)

© Copyright 2006 IEEE – All Rights